## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Sehat Sutardja

Art Unit: Examiner:

Unknown Unknown

Serial No.:

to be assigned HEREWITH

Filed Title

IMPROVED INTEGRATED CHIP PACKAGE HAVING

INTERMEDIATE SUBSTRATE

## BOX PATENT APPLICATION

Commissioner for Patents Washington, D.C. 20231

## INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449, copies of which are enclosed.

This statement is being filed with the application. apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Joseph L. Stevenson

Reg. No. 43,163

ERIC B. JANOFSY Marvel Semiconductor, Inc. 645 Almanor Ave. Sunnyvale, CO 94086

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CERTIFICATE OF MAILING BY EXPRESS MAIL

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I hereby certify under 37 CFR \$1.10 that this correspondence is being deposited with the United States Postal Service as Express Mail Post Office to Addressee with sufficient postage on the date indicated below and is addressed to the Commissioner for D.C. 20231. Patents, Washington,

September Date of Deposit

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Michael Hubbard

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